

Notice of Allowability

Application No.

09/824,863

Examiner

Daniel Pan

Applicant(s)

CHRISTIE, DAVID S.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the paper filed on 11/19/04.
2. ☒ The allowed claim(s) is/are 1,3-11,13,15-42 (claims 2,12,14 have been canceled).
3. ☒ The drawings filed on 02 April 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DANIEL H. PAN
SENIOR EXAMINER
GROUP

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Reasons for Allowance

None of the prior art of record teaches :

- a) the combined features of the register file including the standard registers set and extended register set, the specific operations of the execution core, the less or equal number of the standard registers to the general purpose registers, and the number of extended registers greater than the general purpose registers (claim 1) ;
- b) the combined features of the specific operations of the execution core, the numbers of the standard registers, general purpose registers, the extended registers, the extended prefix byte including the identification to select one of the standard registers and the additional register identification needed to identify the selected one often extended registers (claim 13);
- c) the combined features of the register file including the standard registers comprising the general purpose registers, the greater number of the extended register than the general purpose registers, the greater width of the extended register than the general purpose register , the specific operations of the execution core including the indicating of the CPU in extended mode, the instruction prefix portion including the information needed to access the extended register, the prefix portion indicating the entire contents of the extended register is to be accessed (claim 21);
- d) the combined features of the specific details of the register file including the standard registers and extended registers, the specific operations of the execution core, and the extended prefix byte including the identification to select one of the

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standard registers and the additional register identification needed to identify the selected one of the extended registers (claim 31);

e) the combined features of the register file including the standard registers and extended registers, the specific operations of the execution core, the standard register set of 8 32 bit general purpose registers AX, EBX, ECX, EDX, ESP, EBP, ESI, EDI registers, the extended register set of 8 32 bit general purpose registers of the standard register set and 8 additional 32-bit registers not defined by the x86 architecture (claim 33);

f) the combined features of the register file include the standard registers et and extended register set, the specific operations of the execution core, and the prefix extended registers byte comprising the extended key indicating whether or not the extended register prefix includes the information needed to access the at least one extended register (claim 35);

g) the register file of the standard register set and the extended register set, the specific operations of the execution core, the control registers for indicating the global enablement of the extended register mode, the flag registers for indicating the extended mode enablement by a current process, and the generation of the operating mode of the CPU indicating the CPU extended mode if the extended register mode is globally enabled and enabled by the current process (claim 37);

h) the combined features of the specific features of the register file including the standard register set and the greater number of the extended registers, the specific operations of the execution core, and the standard register set comprising the 32-bit

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general purpose registers defined in x86, and the extended register set includes the 8 32-bit general registers identification to identify the selected one of the standard registers, the prefix extended register byte including the additional register identification needed to identify the selected extended register (claim 38);

i)) the combined features of the specific features of the register file including the standard register set and the greater number of the extended registers, the specific operations of the execution core, and the standard register set comprising the 32-bit general purpose registers defined in x86, and the prefix extended registers byte comprising the extended key indicating whether or not the extended register prefix includes the information needed to access the at least one extended register (claim 40);

j) the combined features of the specific features of the register file including the standard register set and the greater number of the extended registers, the specific operations of the execution core, and the standard register set comprising the 32-bit general purpose registers defined in x86, the control registers for indicating the global enablement of the extended register mode, the flag registers for indicating the extended mode enablement by a current process, and the generation of the operating mode of the CPU indicate the CPU extended mode if the extended register mode is globally enabled and enabled by the current process (claim 42);

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dan Pan whose telephone number is 703 305 9696, or

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the new number 571 272 4172. The examiner can normally be reached on M-F from 8:30 AM to 4:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chan, can be reached on 703 305 9712, or the new number 571 272 4162. The fax phone number for the organization where this application or proceeding is assigned is 703 306 5404.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

21 Century Strategic Plan

DANIEL H. PAN
PRIMARY EXAMINER
SECUP

